



Docket No.: ESEC-P220US

*IFW*

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT: Bollinger, Daniel  
SERIAL NO.: 10/719,829  
FILING DATE: November 20, 2003  
TITLE: Method For Picking Semiconductor Chips From A Foil  
EXAMINER: GREENHUT, CHARLES N  
ART UNIT: 3652

---

**COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450**

**STATUS REQUEST LETTER**

Please advise the status of the above-identified patent application.

Respectfully submitted,  
THELEN REID & PRIEST LLP

Dated: March 7, 2006

---

David B. Ritchie  
Reg. No. 31,562

Thelen Reid & Priest LLP  
P.O. Box 640640  
San Jose, CA 95164-0640  
Tel.: 408.292.5800  
Fax: 408.287.8040